

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Rule 53(b) Divisional Application of:

Naoki NAKAMURA

Serial No.: 10/274,125

Group Art Unit: 2827

Filed: September 17, 2003

Examiner: PATEL, Ishwarbhal B.

For: MULTILAYER WIRING BOARD, MANUFACTURING METHOD  
THEREFOR AND TEST APPARATUS THEREOF

**INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 CFR 1.97(b)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 22, 2003

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 10/274,125.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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**23850**

PATENT TRADEMARK OFFICE

Enclosure: PTO-1449

<b>INFORMATION DISCLOSURE CITATION PTO-1449</b>	Atty. Docket No. 021385B	Serial No. Div. of 10/274,125
	Applicant(s): Naoki NAKAMURA	
	Filing Date: September 22, 2003	Group Art Unit: 2827

#### U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
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